

Notice of Allowability	Application No.	Applicant(s)	
	10/641,702	GERLACH ET AL.	
	Examiner	Art Unit	
	Benny Q. Tieu	2614	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Patent Application filed Aug. 15, 2003.
2. ☒ The allowed claim(s) is/are 1-34.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____ |
|---|--|

Reasons For Allowance

1. The following is an examiner's statement of reasons for allowance: Boike et al. (U.S. Patent No. 6,959,376) teach a system-on-a-chip (SoC) integrated circuit containing multiple digital signal processors (DSPs). In an embodiment of the invention, the integrated circuit includes two or more DSPs and a single host processor interface. Each DSP includes its own memory unit and a direct memory access (DMA) device (column 2, lines 3-8). Blackburn et al. (U.S. Patent No. 7,023,877) teach an apparatus for multiplexing a digital data input into DSL outputs. The apparatus may include a transceiver for providing connectivity to a digital data line providing 24 DS0 channels. The apparatus may also include at least one DSL transceiver for multiplexing DS0 channels onto corresponding DSL-compatible transmission media using DSL technology (Abstract). However, prior art of record fails to teach, or renders obvious, alone or in combination, an integrated digital subscriber line transceiver includes a single integrated circuit having a digital signal processing engine adapted to couple the transceiver to at least one digital subscriber line communication link. The single integrated circuit also includes a time division multiplexing framer adapted to couple the transceiver to at least one time division multiplexing communication link. The single integrated circuit further includes a static random access memory and a microprocessor. The microprocessor is coupled to the digital signal processing engine, the time division multiplexing framer, and the static random access memory as recited in independent claims 1 and 10. Prior art of record fails to teach, or renders obvious, alone or in combination, an integrated digital subscriber line transceiver includes a single integrated circuit having a digital signal processing engine adapted to couple the transceiver to at least one digital

subscriber line communication link and to at least one time division multiplexing communication link. The single integrated circuit further includes a static random access memory and a microprocessor. The microprocessor is coupled to the digital signal processing engine and the static random access memory as recited in independent claims 19 and 26.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Benny Q. Tieu whose telephone number is 571-272-7490. The examiner can normally be reached on Monday-Friday: 6:30AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wing Chan can be reached on 571-272-7493. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

A handwritten signature in black ink, appearing to read 'Benny Q. Tieu', with a stylized, cursive script.

Benny Q. Tieu
Primary Examiner
Art Unit 2614